

ABSTRACT OF THE DISCLOSURE

The present invention relates to a display driving system. In this system, a plurality of memories capable of synchronously being read and written are used as frame 5 buffers. When the image data is transmitted to the memory, the memory will synchronously output the image data to the display device. Therefore, in the inventive system, the memory capacity required for driving can be reduced and the circuit and control system can be simplified.

10 This display driving system comprises a plurality of memories capable of synchronously being read and written to be used as buffers for frame accessing, an image signal generator for processing digital image, a timing controller for being a memory and for timing controlling, a plurality of 15 data drivers for receiving the image data and displaying it on the display device.